

FIG. 1

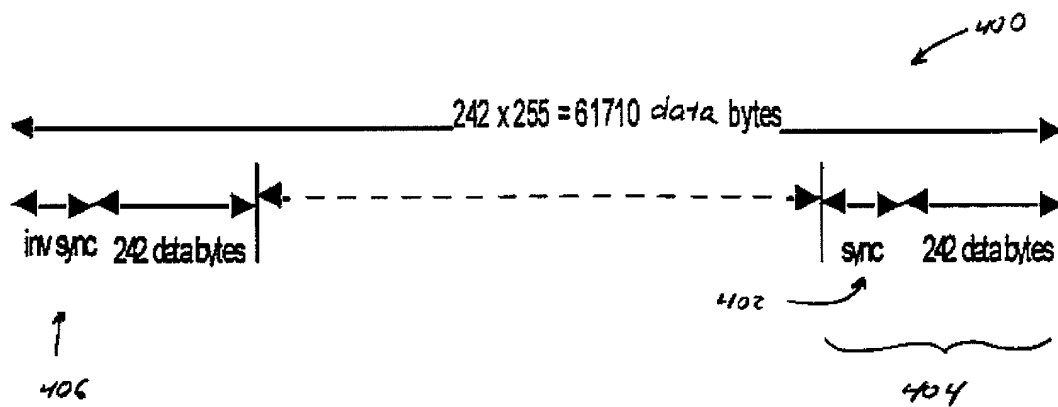


FIG. 4

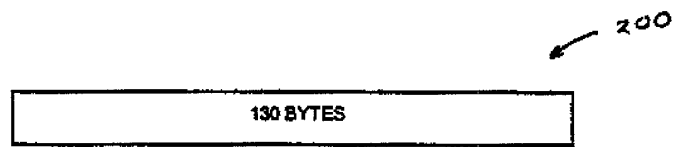


FIG. 2

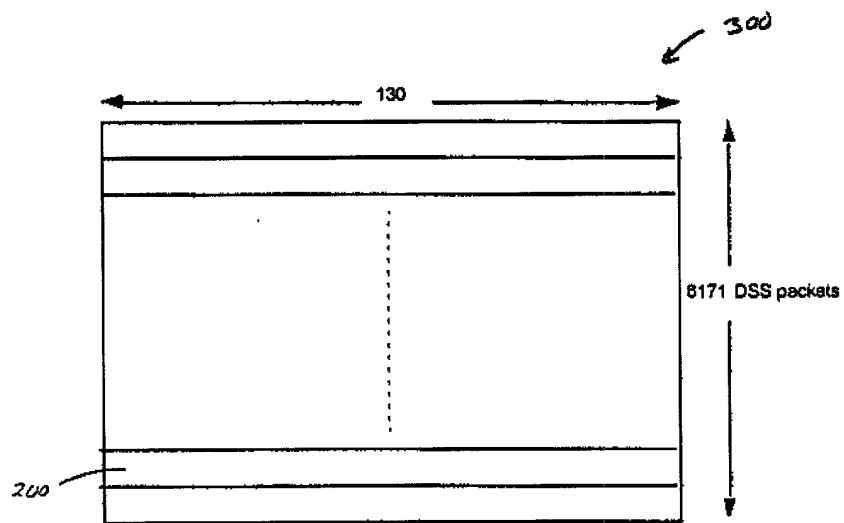


FIG. 3

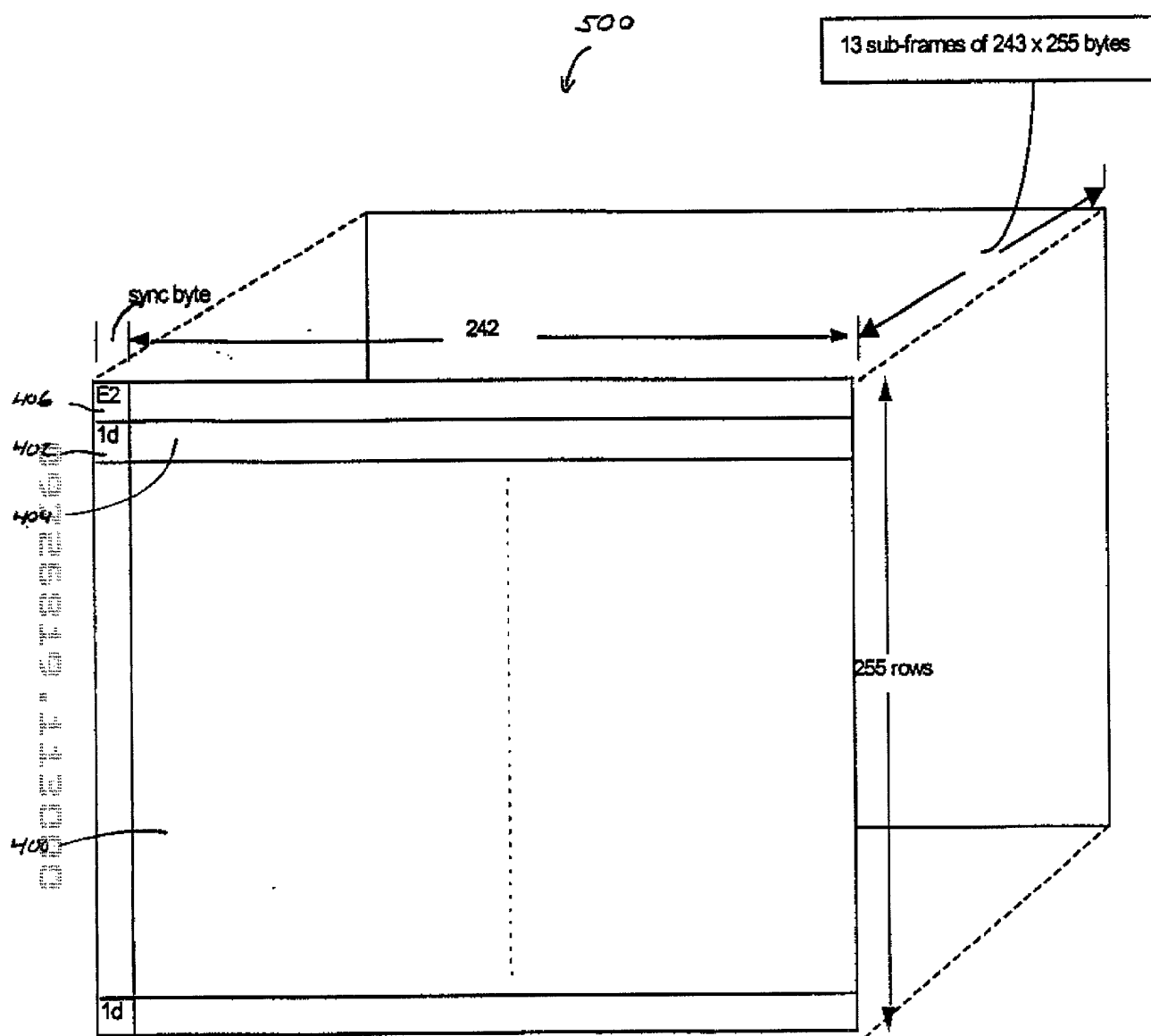


FIG. 5

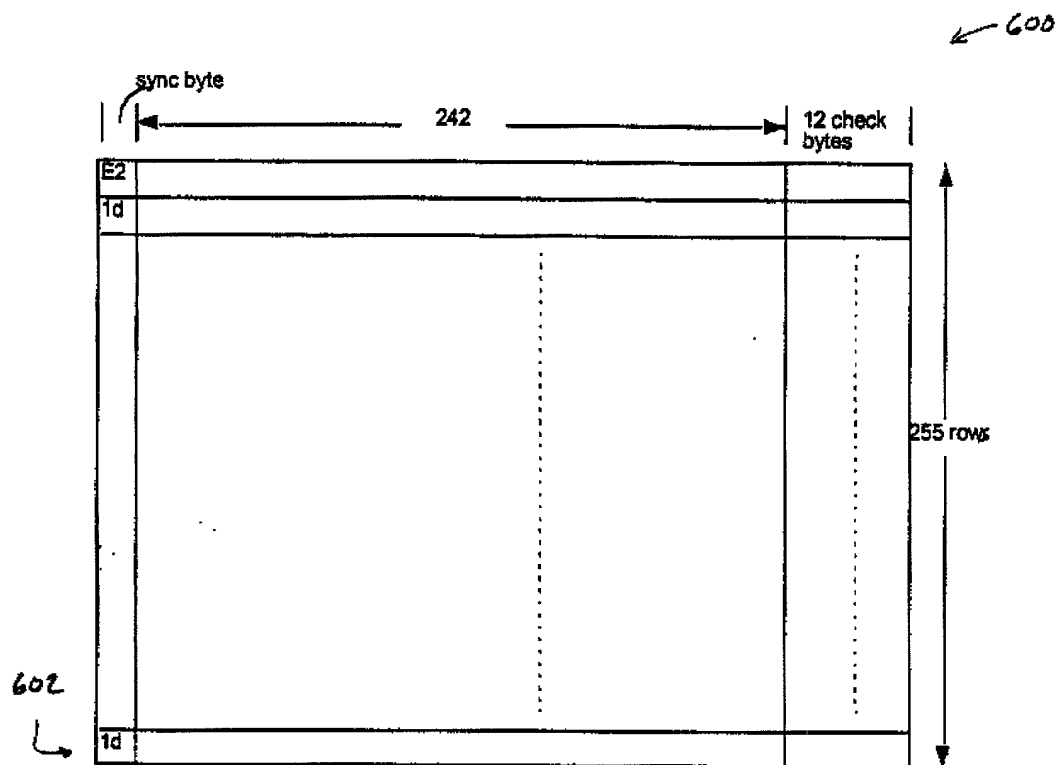


FIG. 6

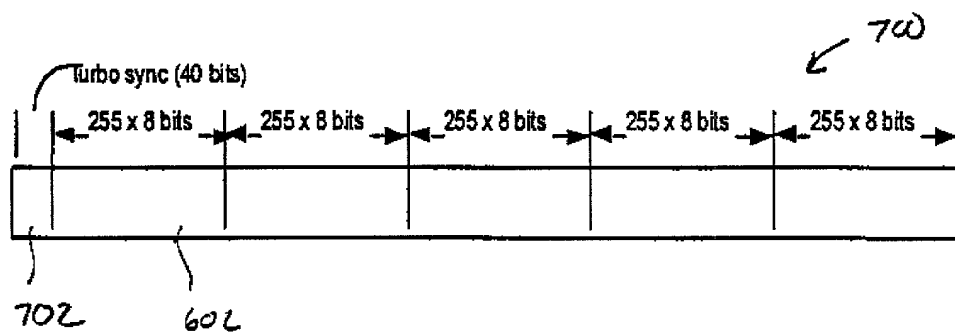


FIG. 7

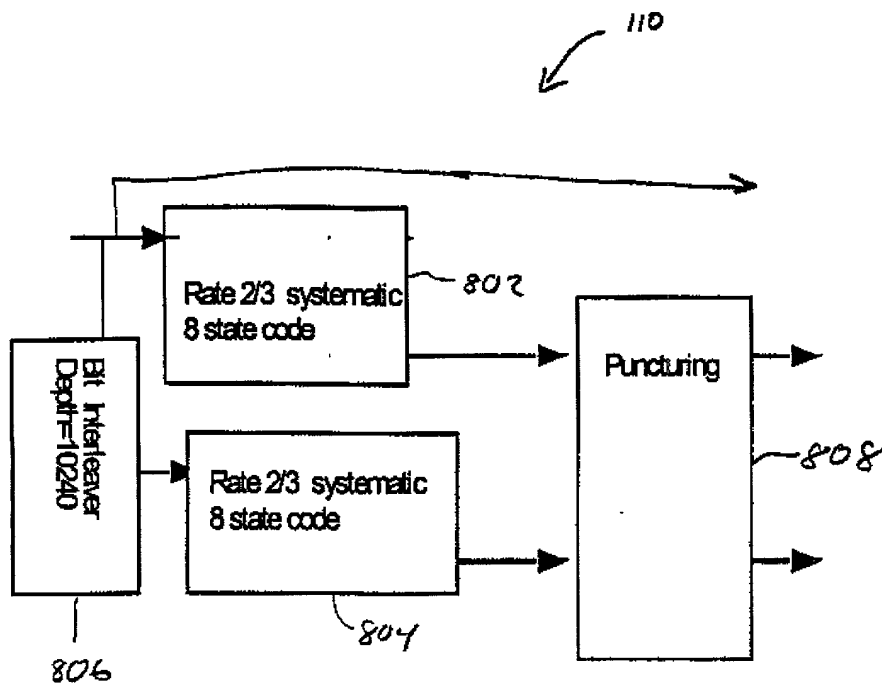


FIG. 8

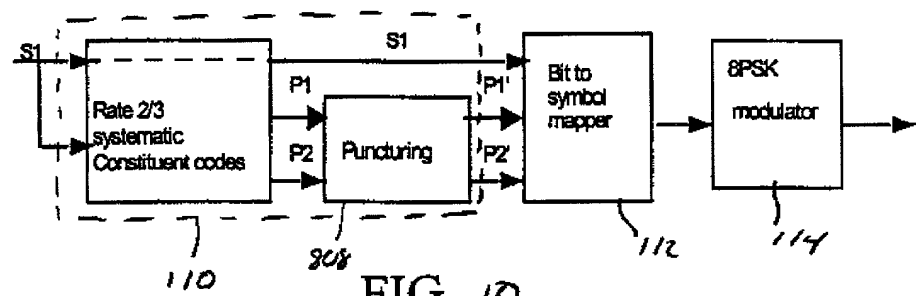


FIG. 10

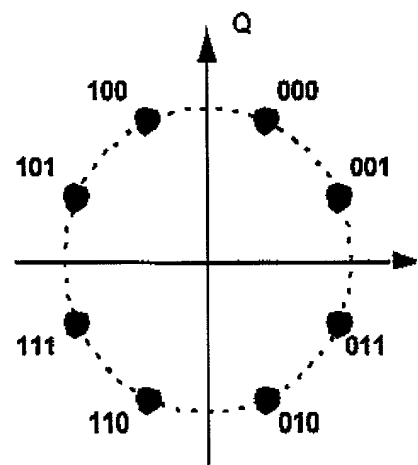


FIG. 11

FIG. 9a

$\underline{S1} \rightarrow$	(2 bits)	(2 bits)
$\underline{P1} \rightarrow$	1	X
$\underline{P2} \rightarrow$	X	1

FIG. 9b

$\underline{S1} \rightarrow$	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)
$\underline{P1} \rightarrow$	1	X	X	X	X
$\underline{P2} \rightarrow$	X	1	X	X	X

FIG. 9c

$\underline{S1} \rightarrow$	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)
$\underline{P1} \rightarrow$	1	X	X	X	X	X	X
$\underline{P2} \rightarrow$	X	1	X	X	X	X	X

FIG. 9d

$\underline{S1} \rightarrow$	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)	(2 bits)
$\underline{P1} \rightarrow$	1	X	X	X	X	X	X
$\underline{P2} \rightarrow$	1	X	X	X	X	X	X

FIG. 9e

$\underline{S1} \rightarrow$	1	1	1	1
$\underline{P1} \rightarrow$	1	X	X	X
$\underline{P2} \rightarrow$	X	X	1	X

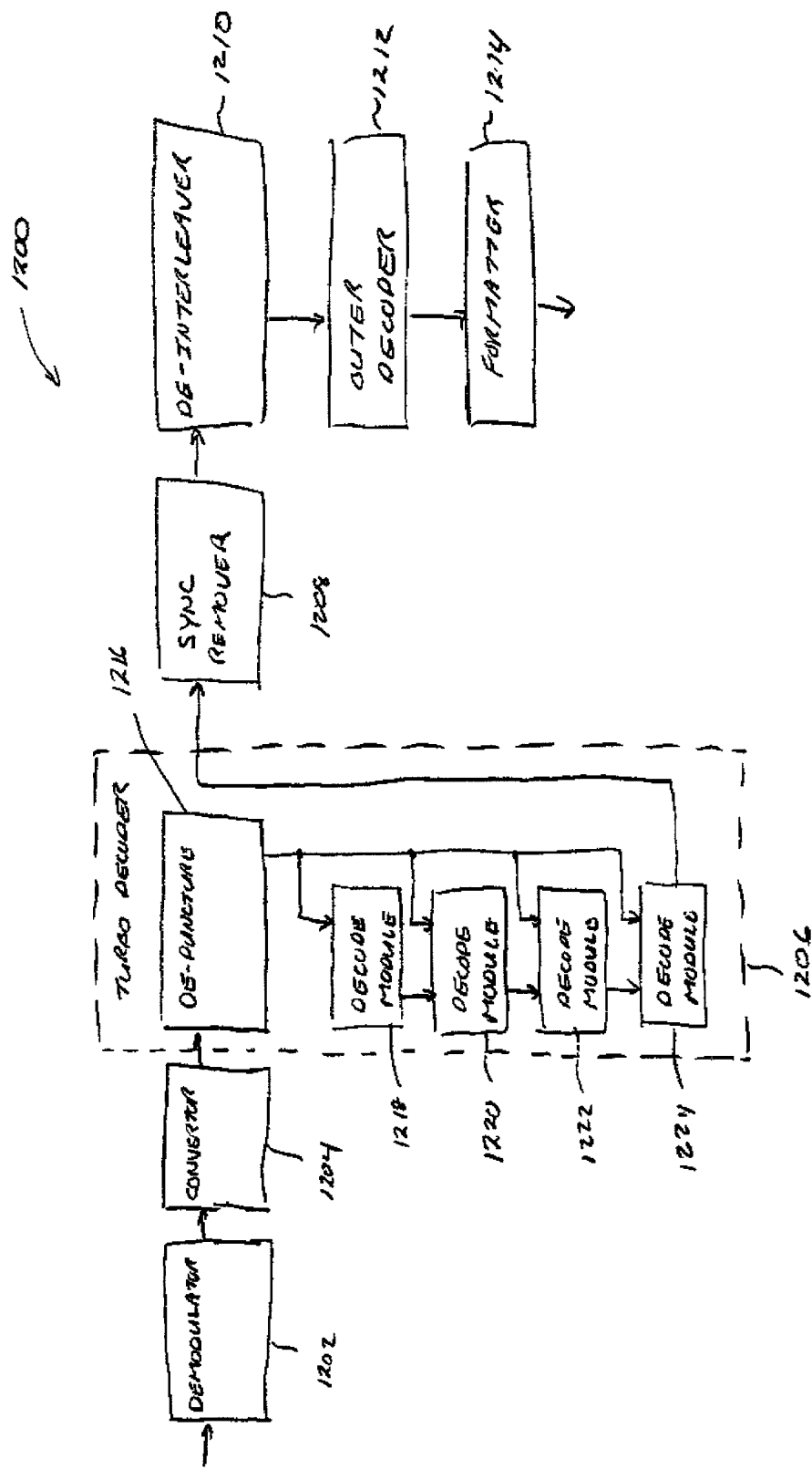


FIG. 12

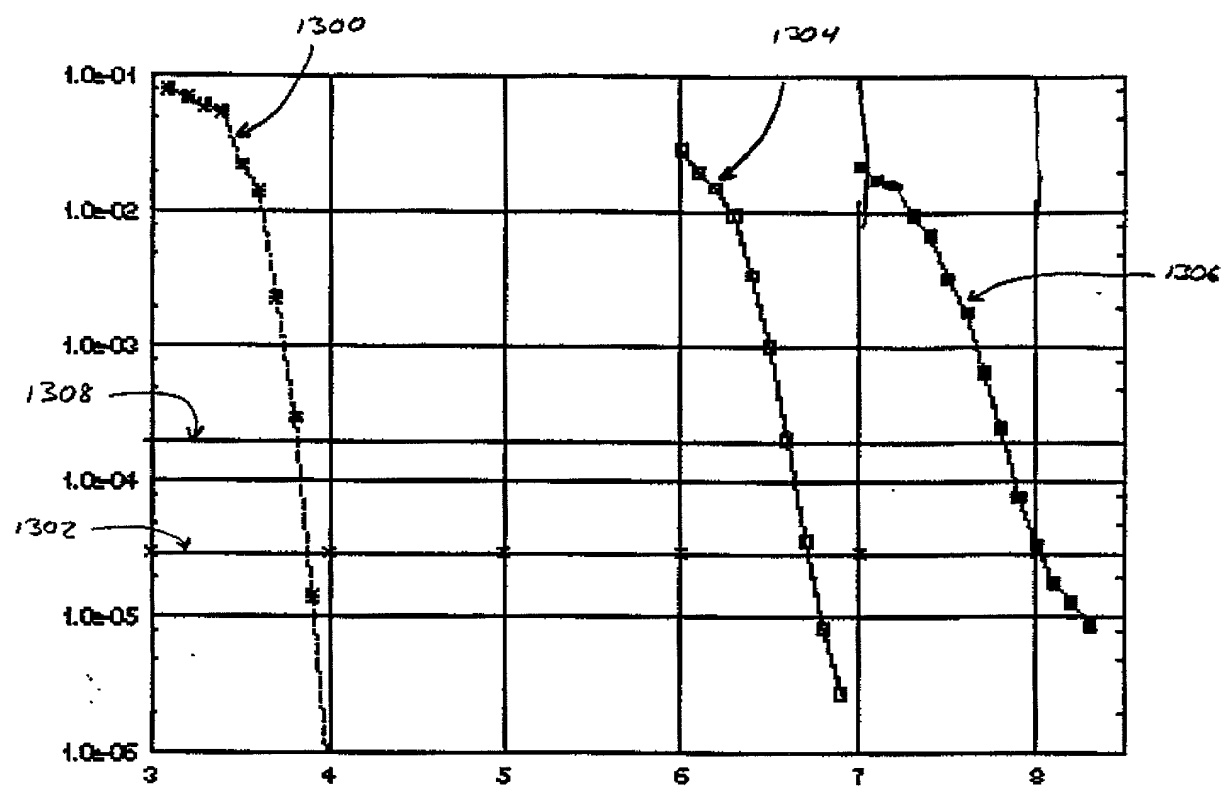


FIG. 13